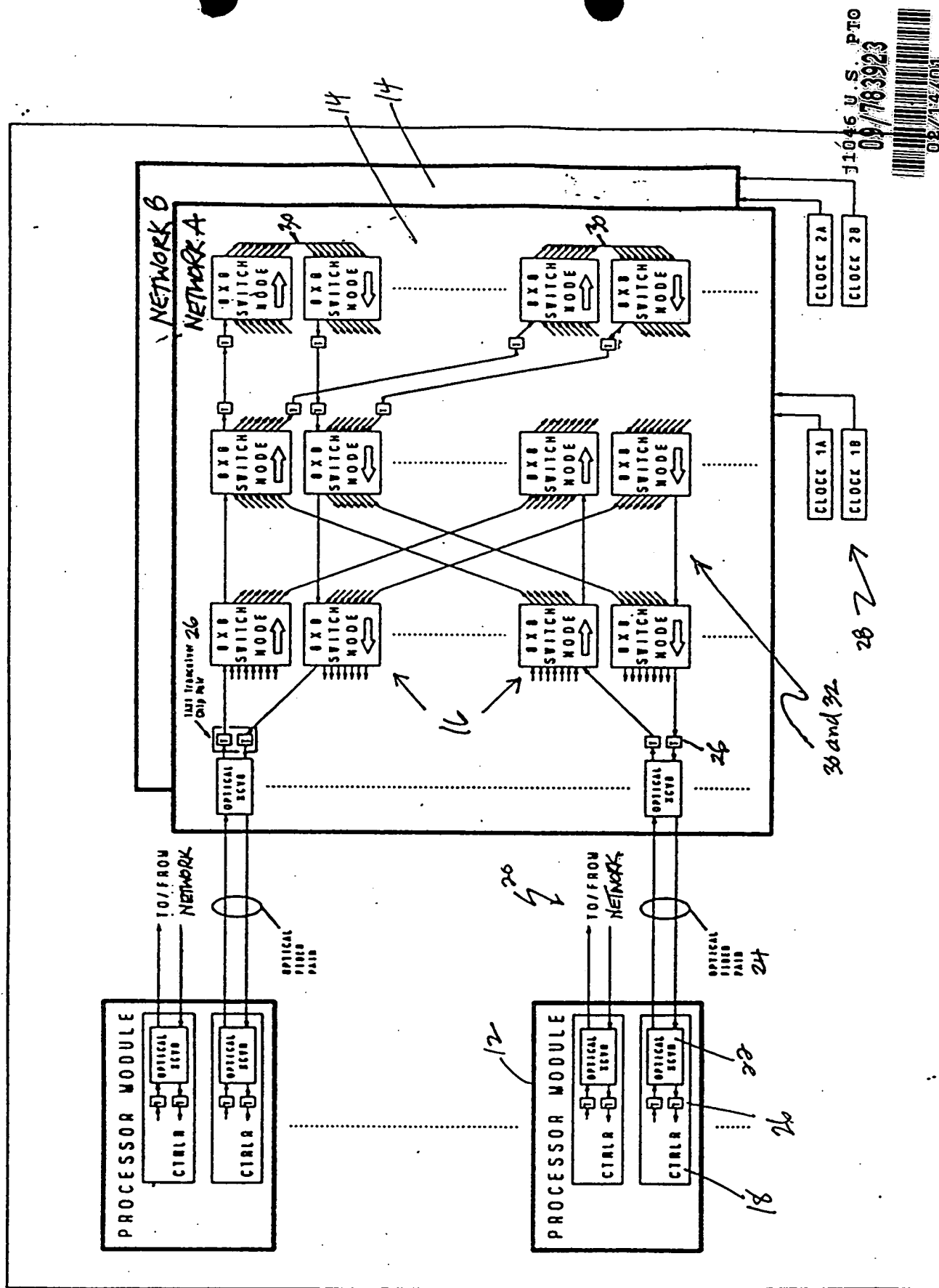


Figure 1



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Figure 2

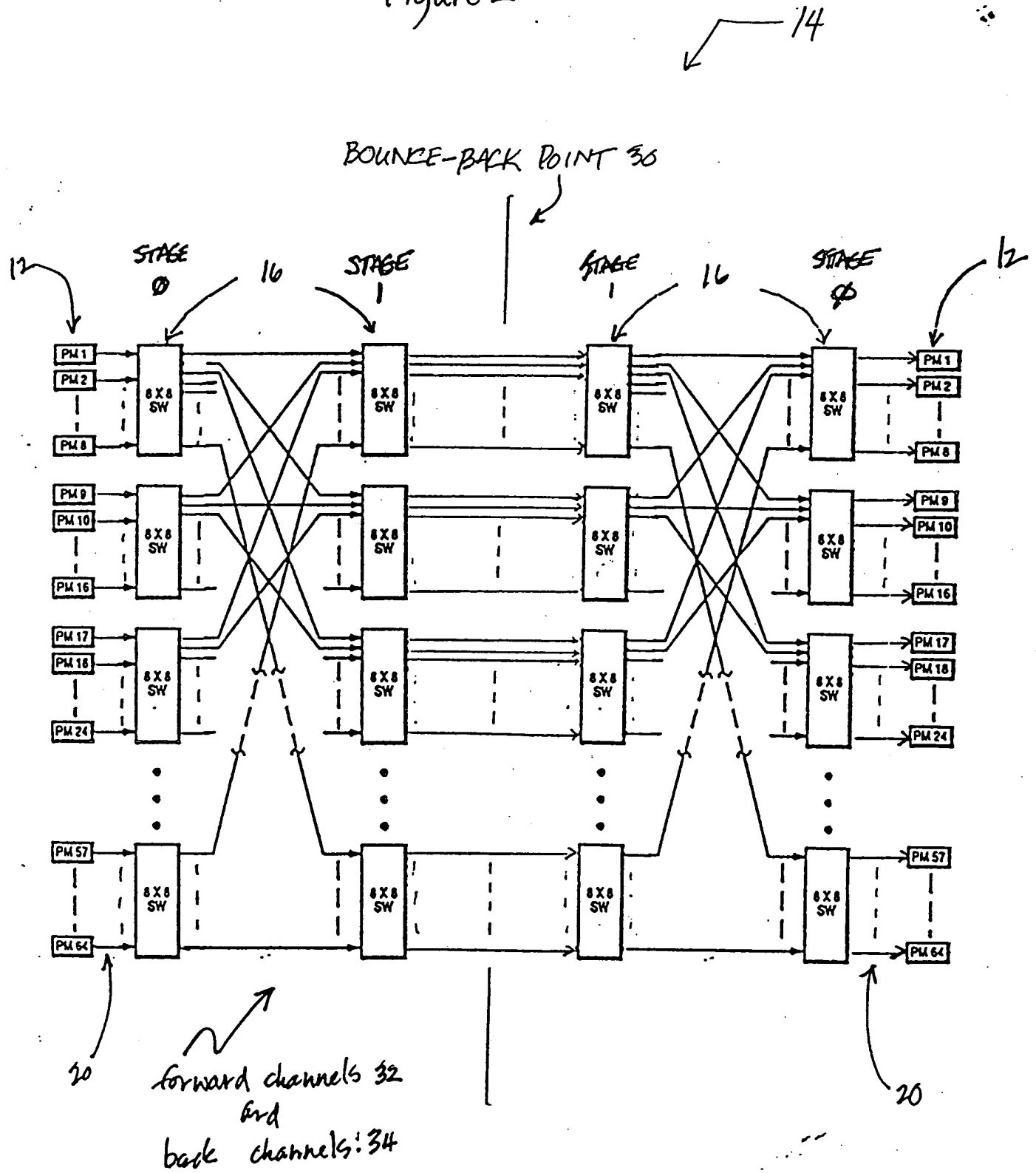


Figure 3

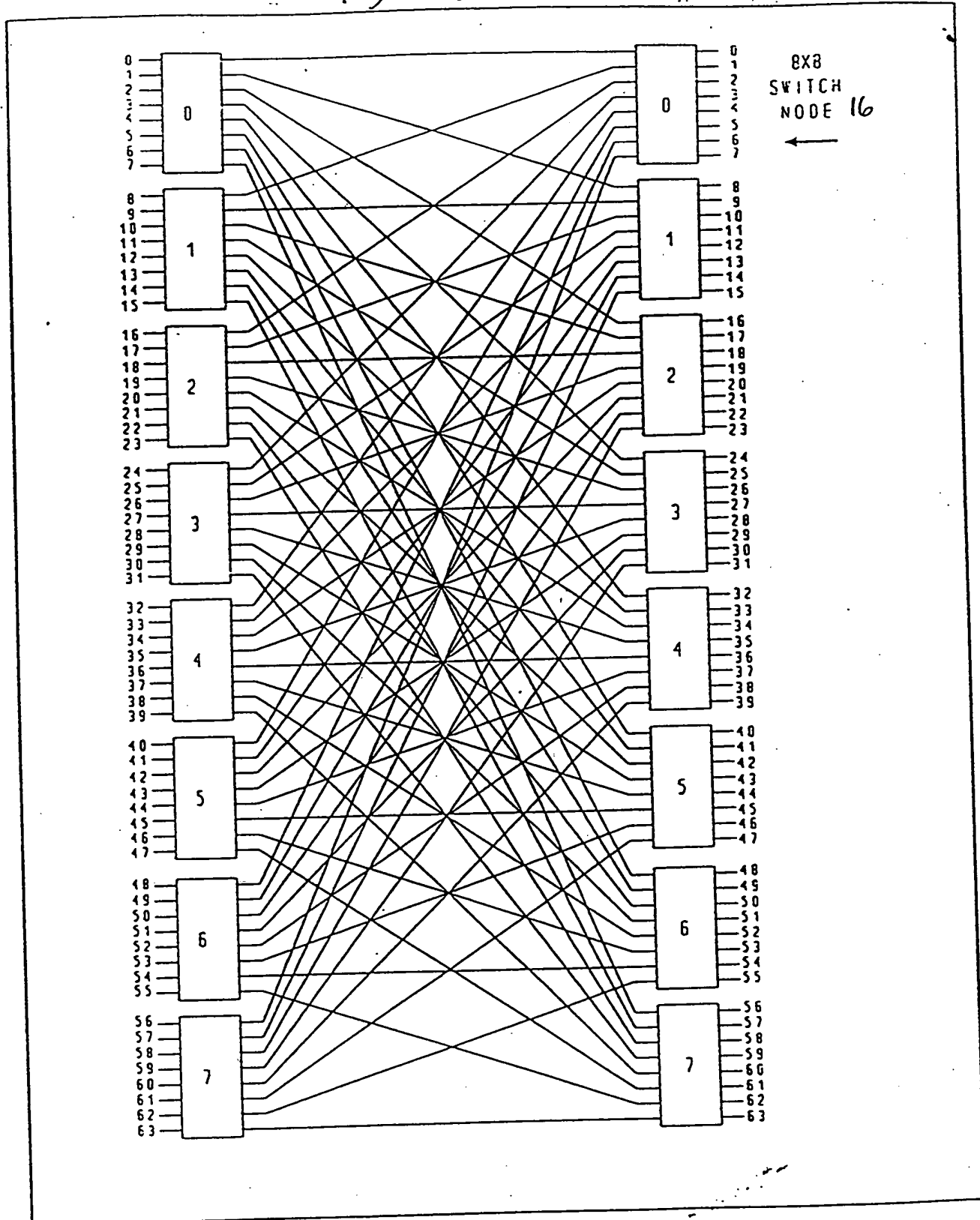


Figure 4

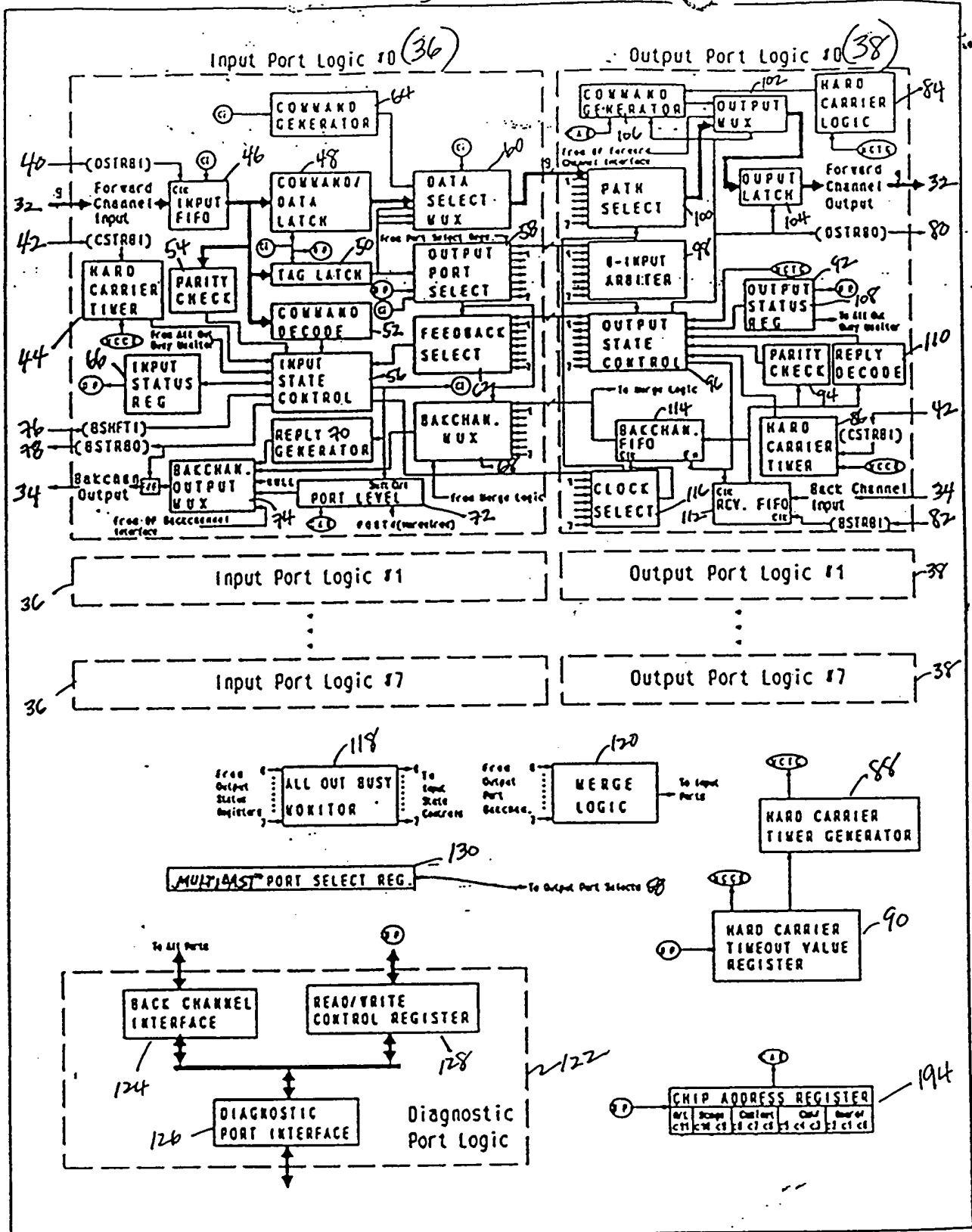


Figure 5

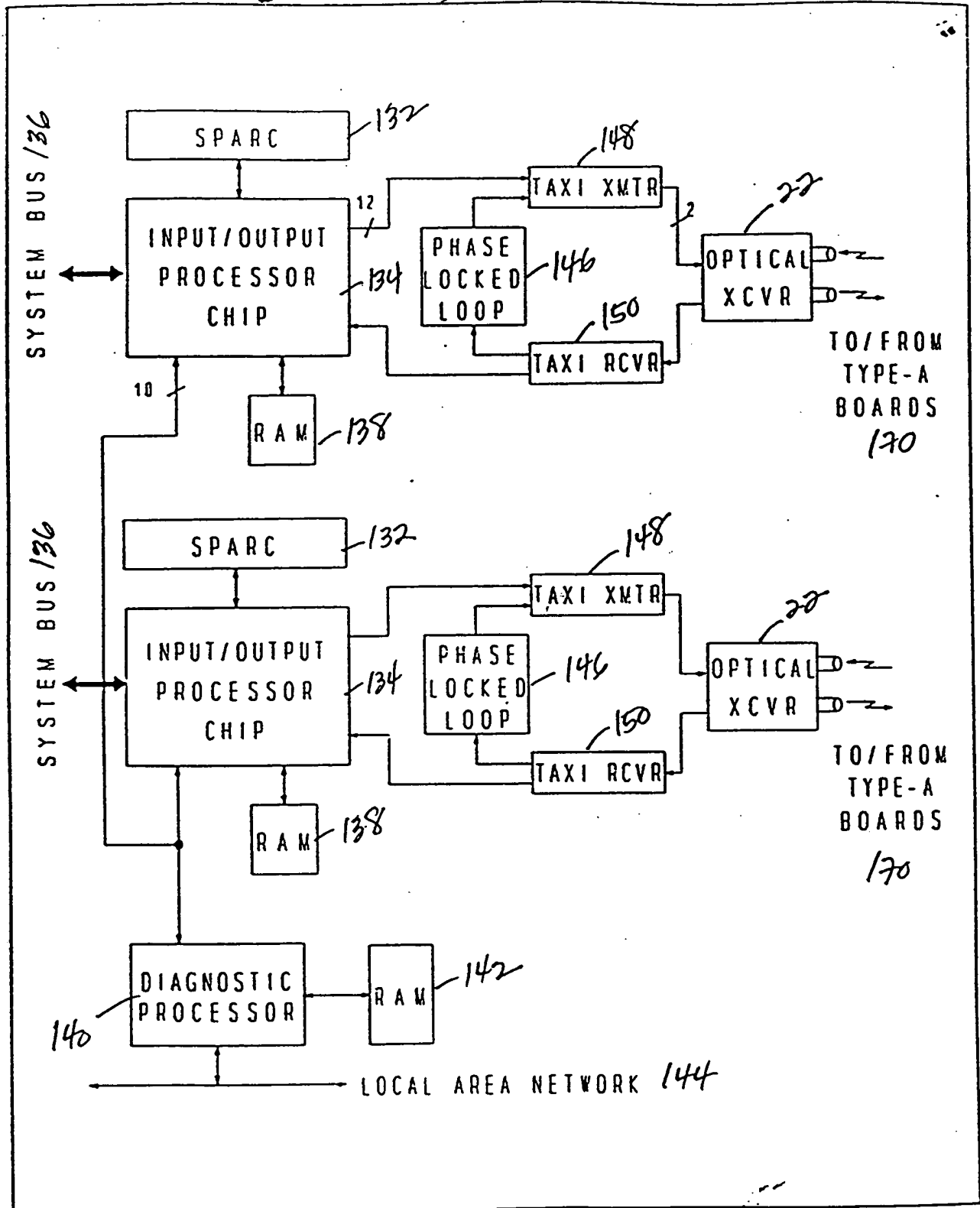


Figure 6

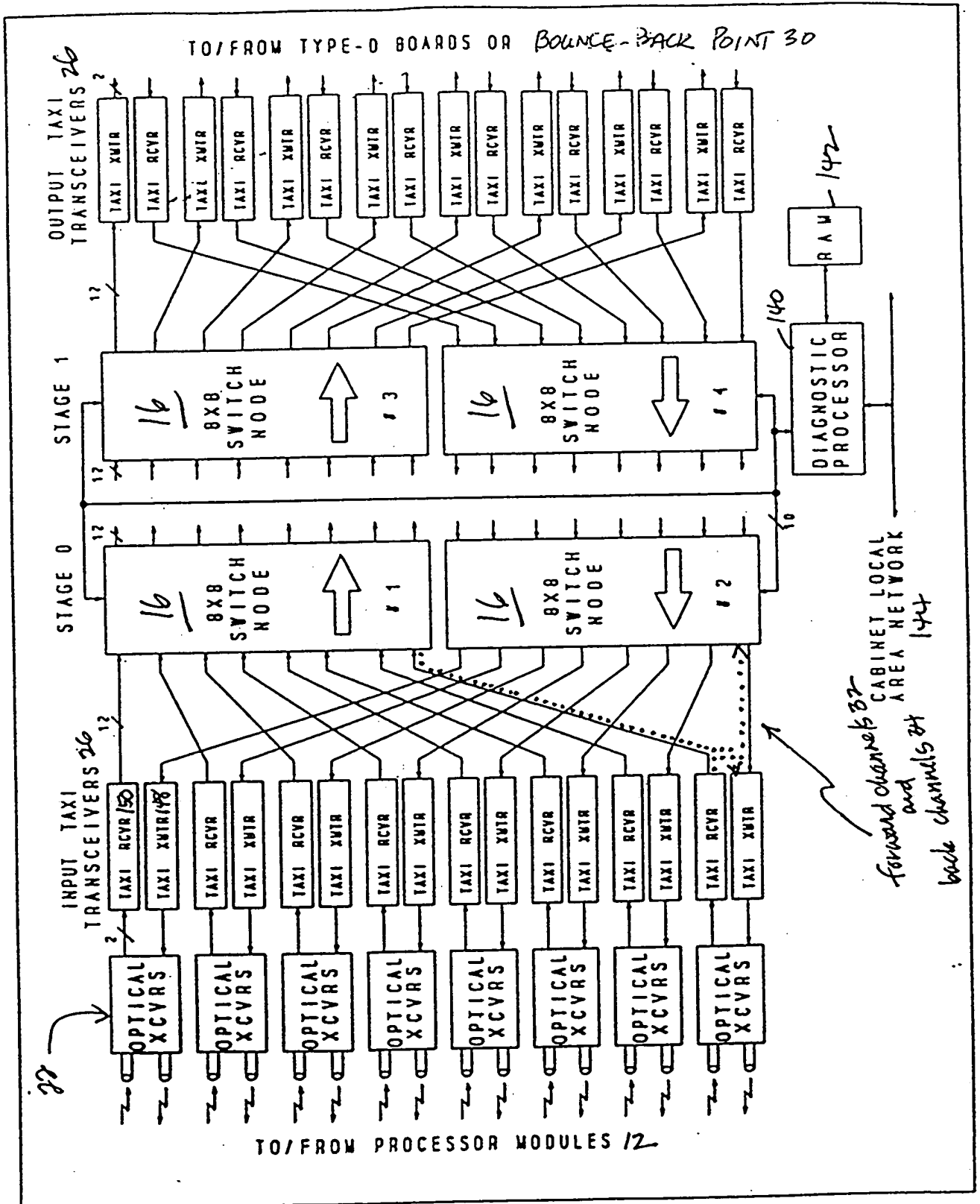


Figure 7

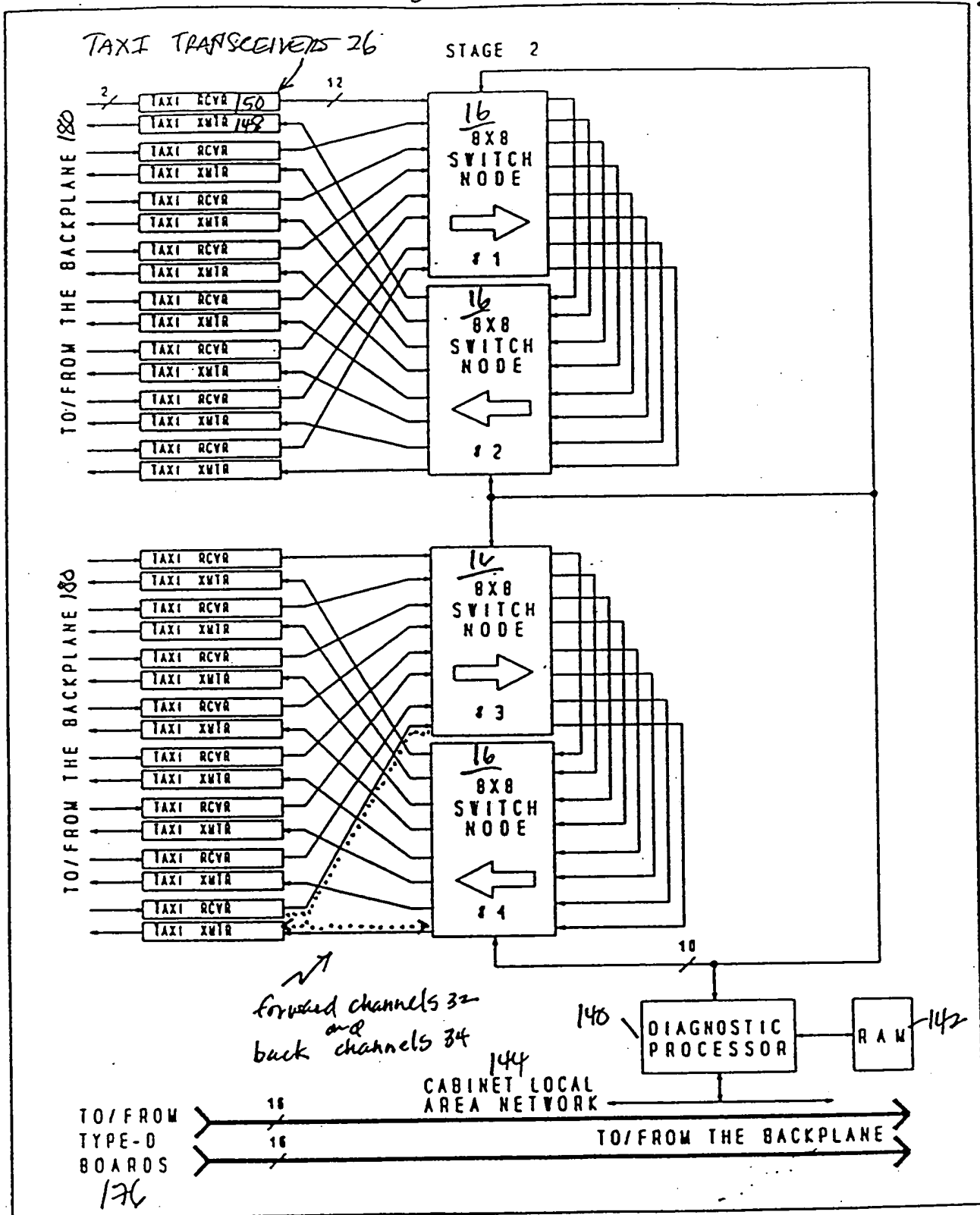


Figure 8 ✓ 174

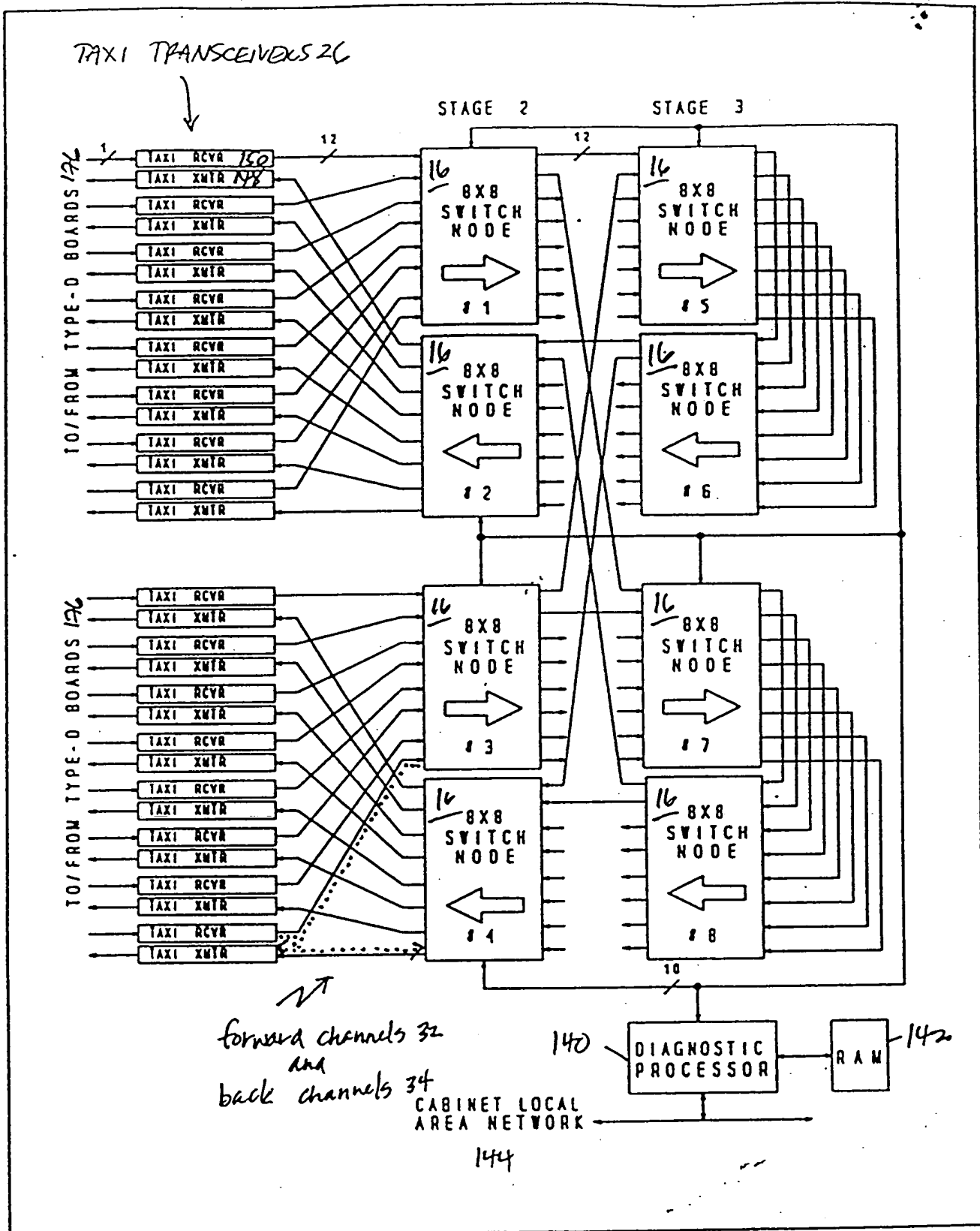


Figure 9
(≤ 64 PORTS)

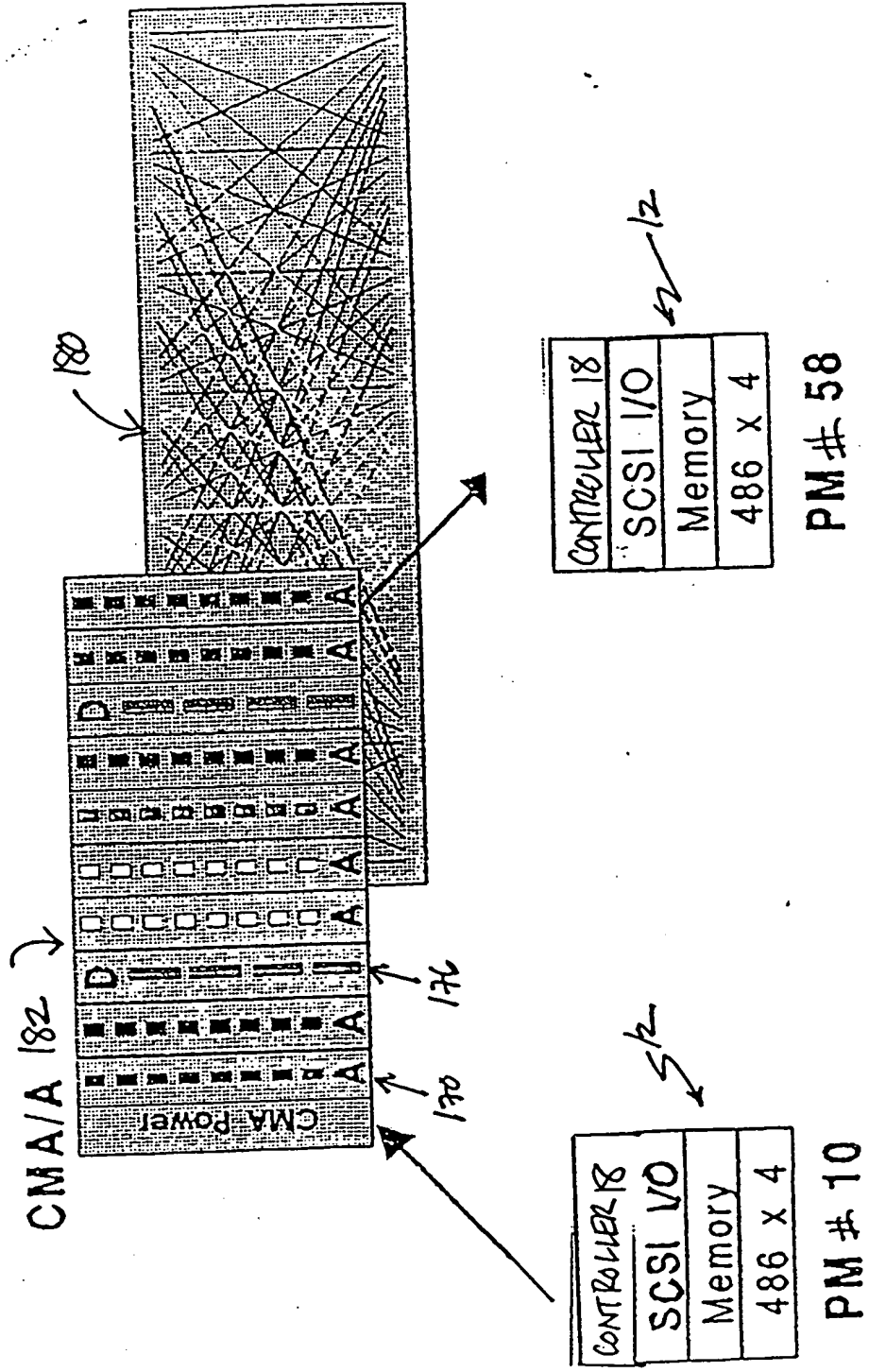


Figure 10

CMA/A 182 Circuit Switching

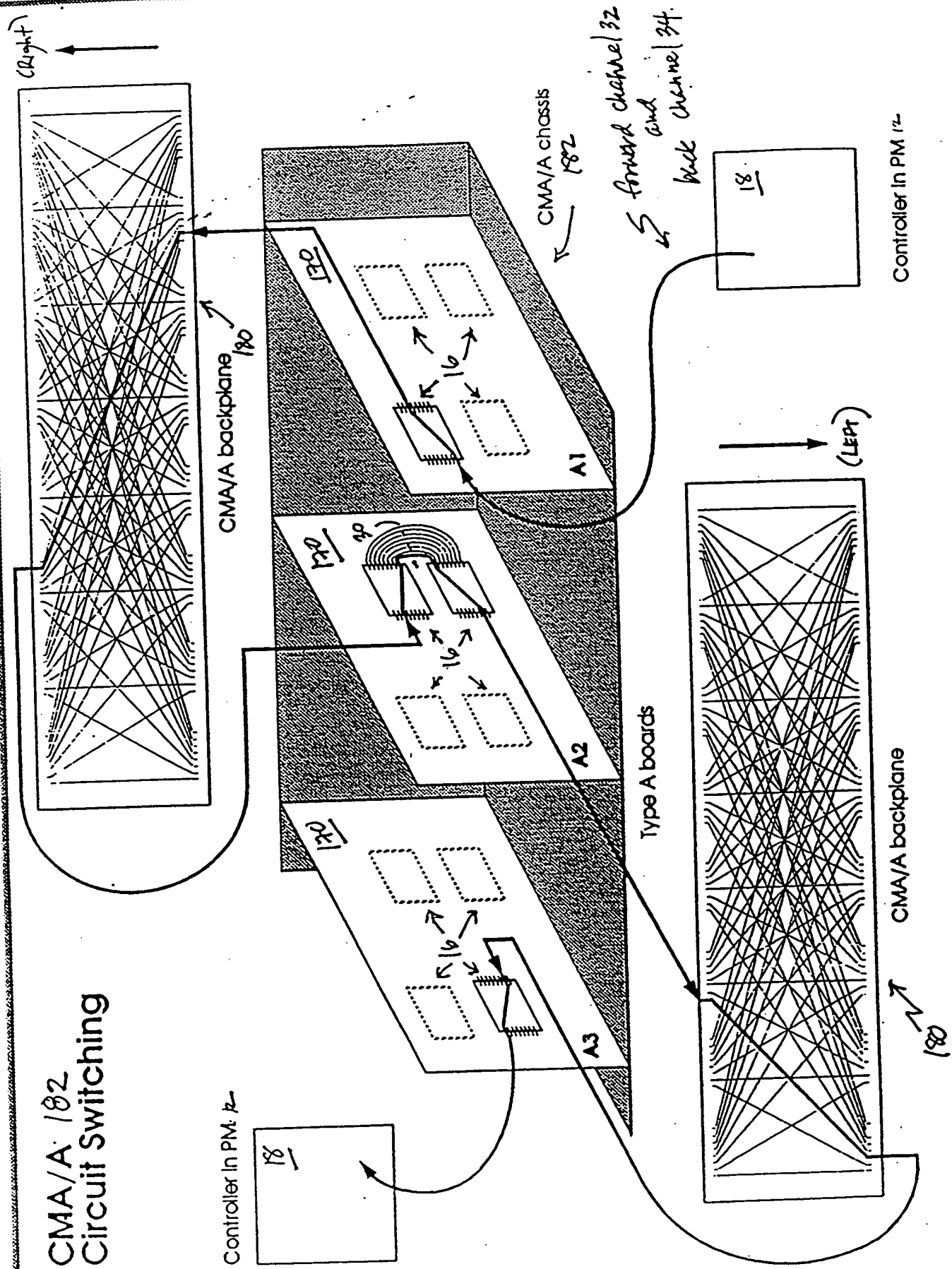


Figure 11
 $(64 < \text{PORTS} \leq 512)$

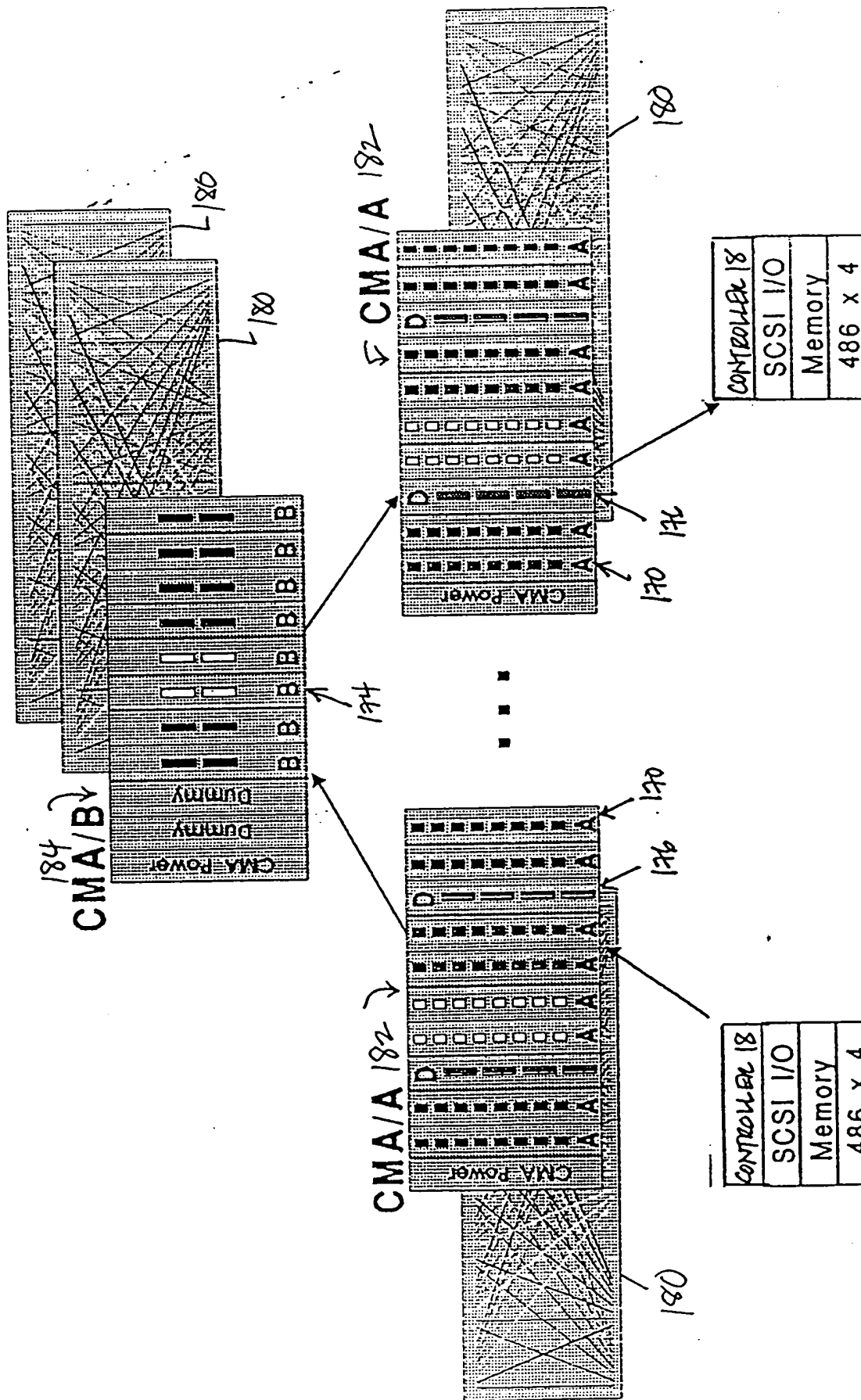
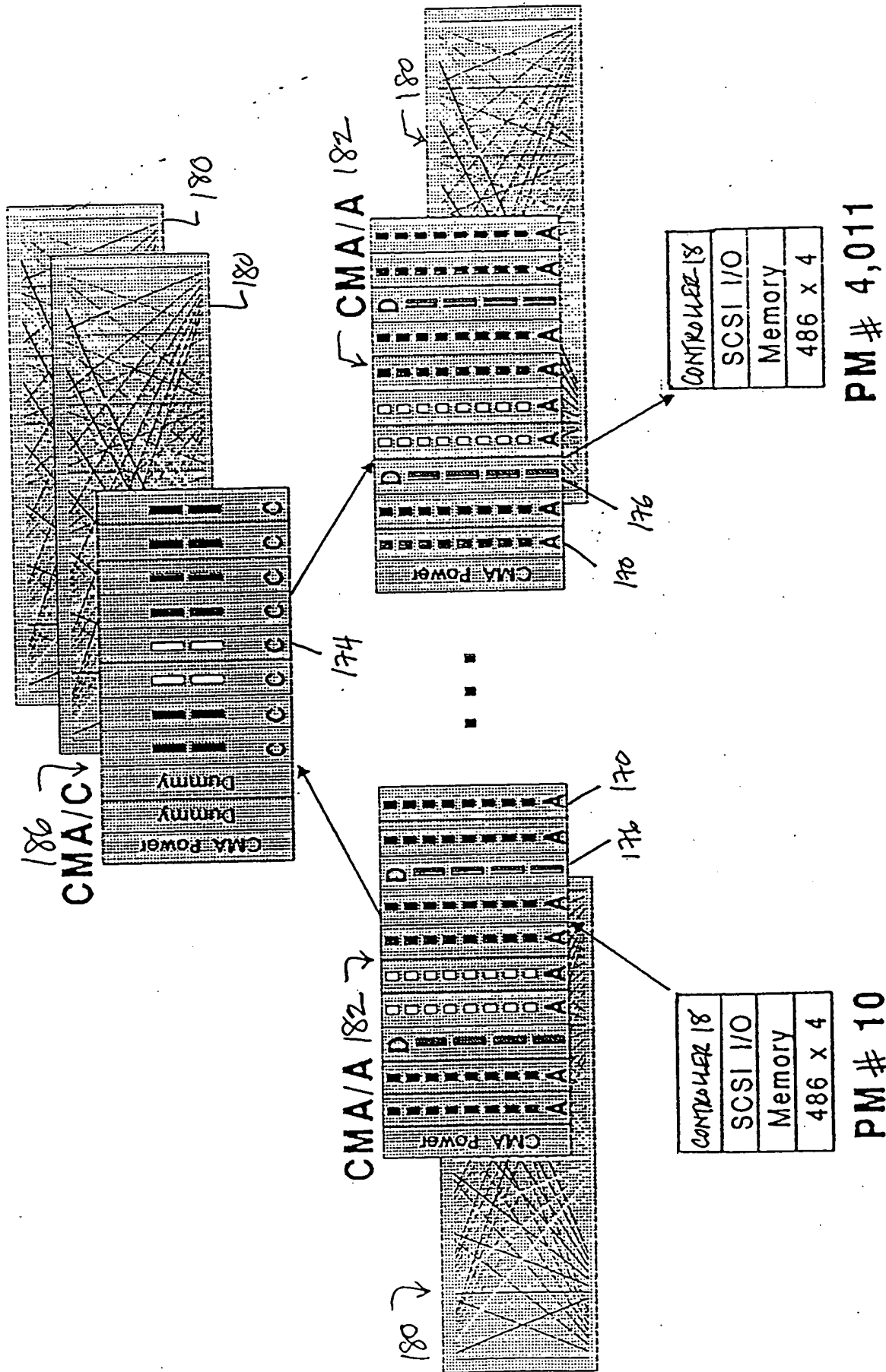


Figure 12
 (512 < Ports ≤ 4,096)



PM # 4,011

PM # 10

Figure 13

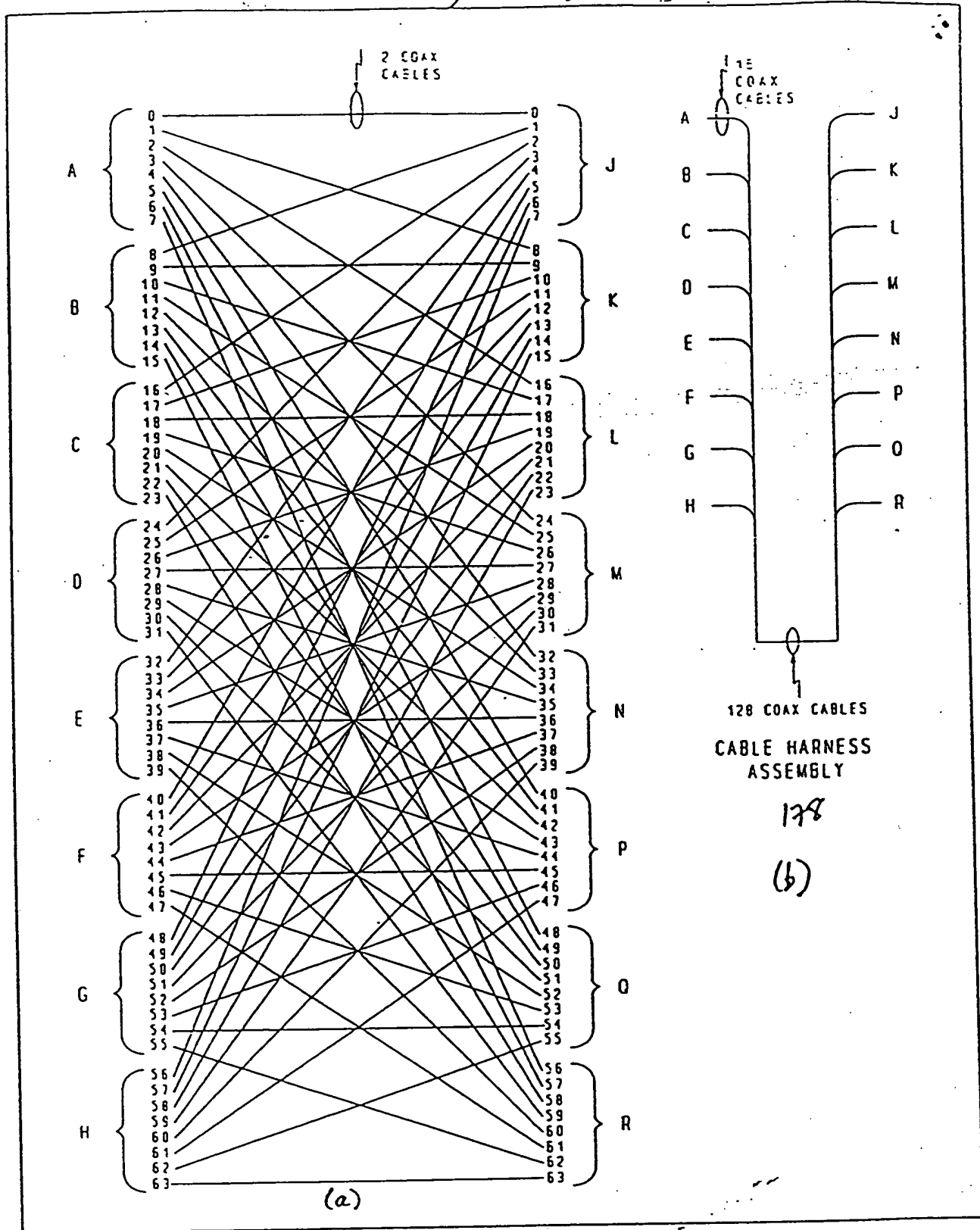


Figure 14

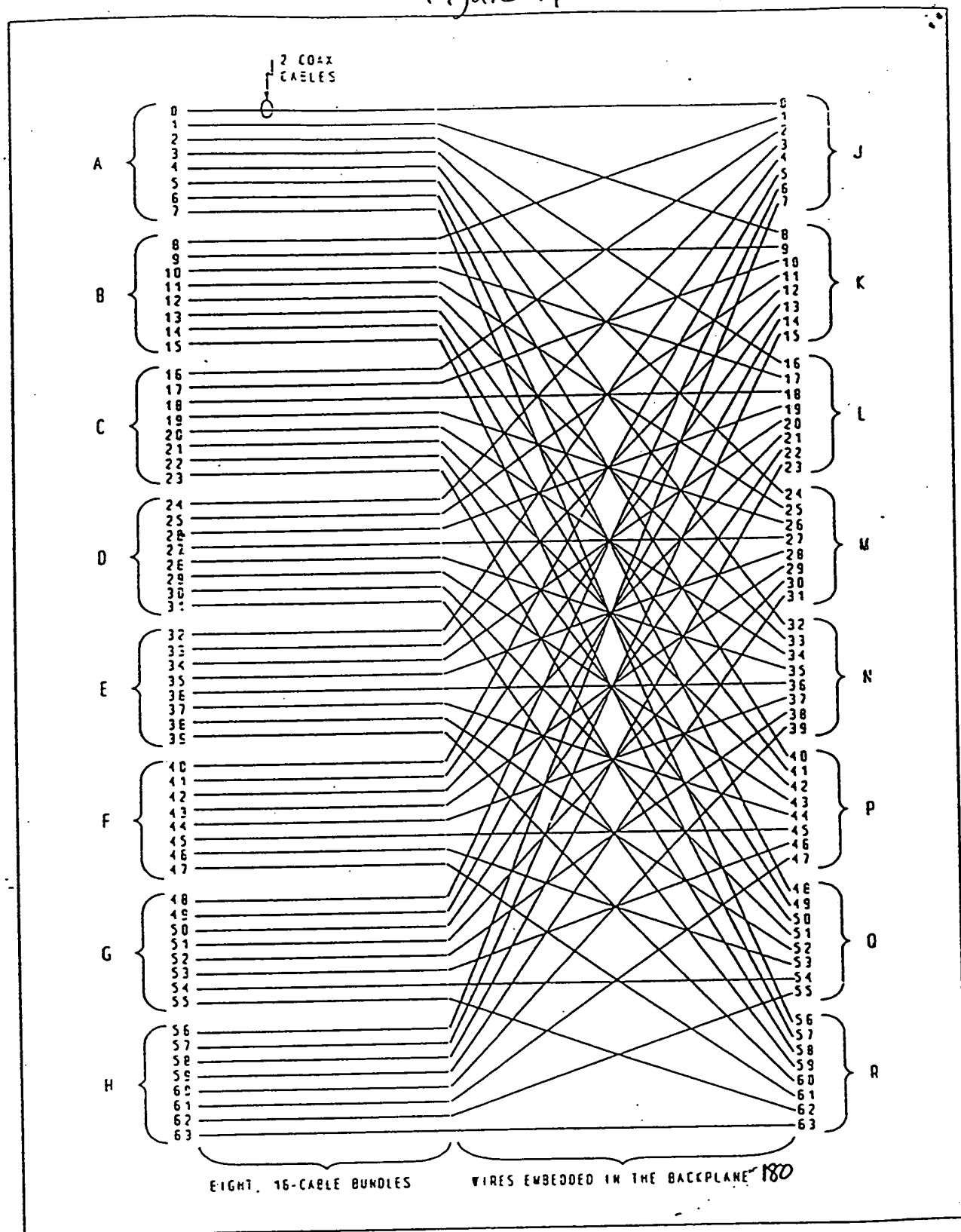


Figure 15

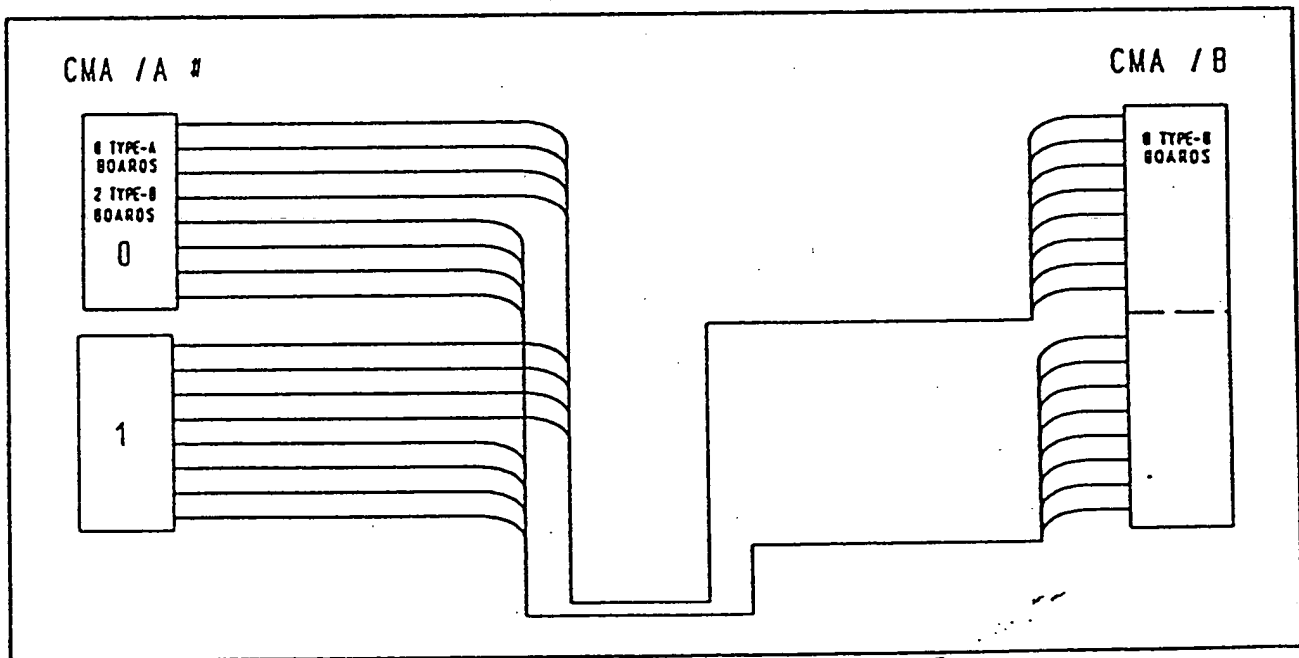


Figure 16

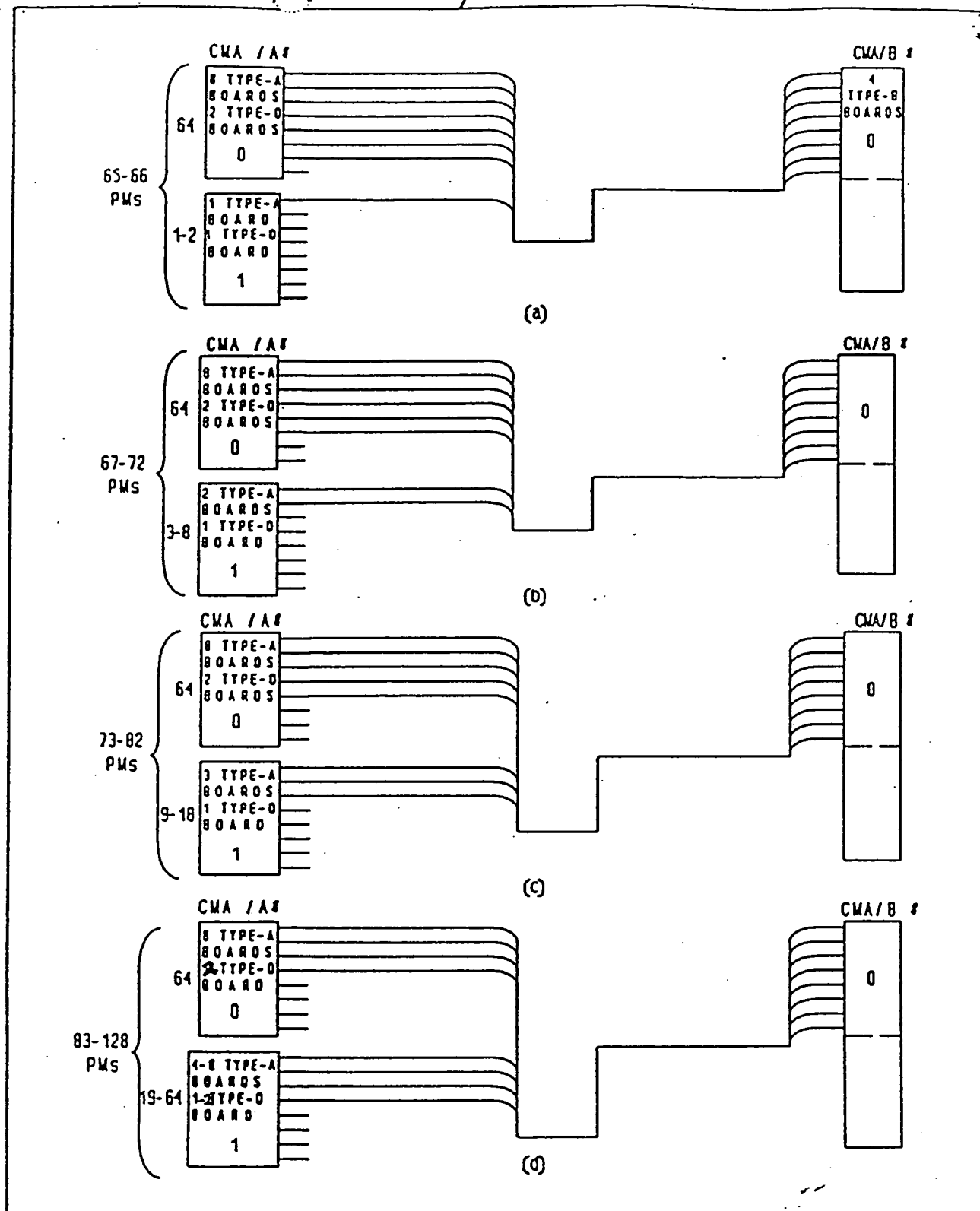


Figure 17

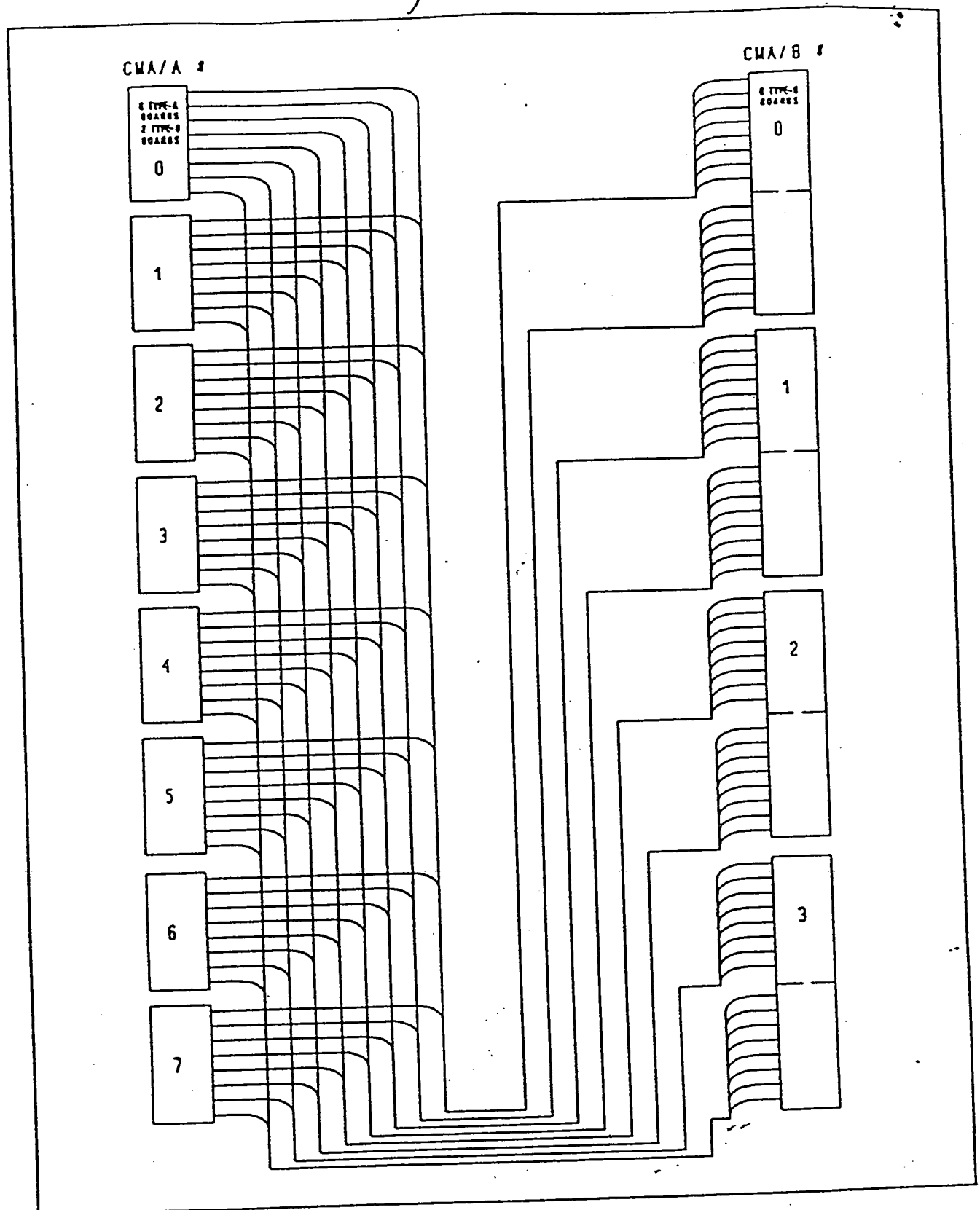


Figure 18

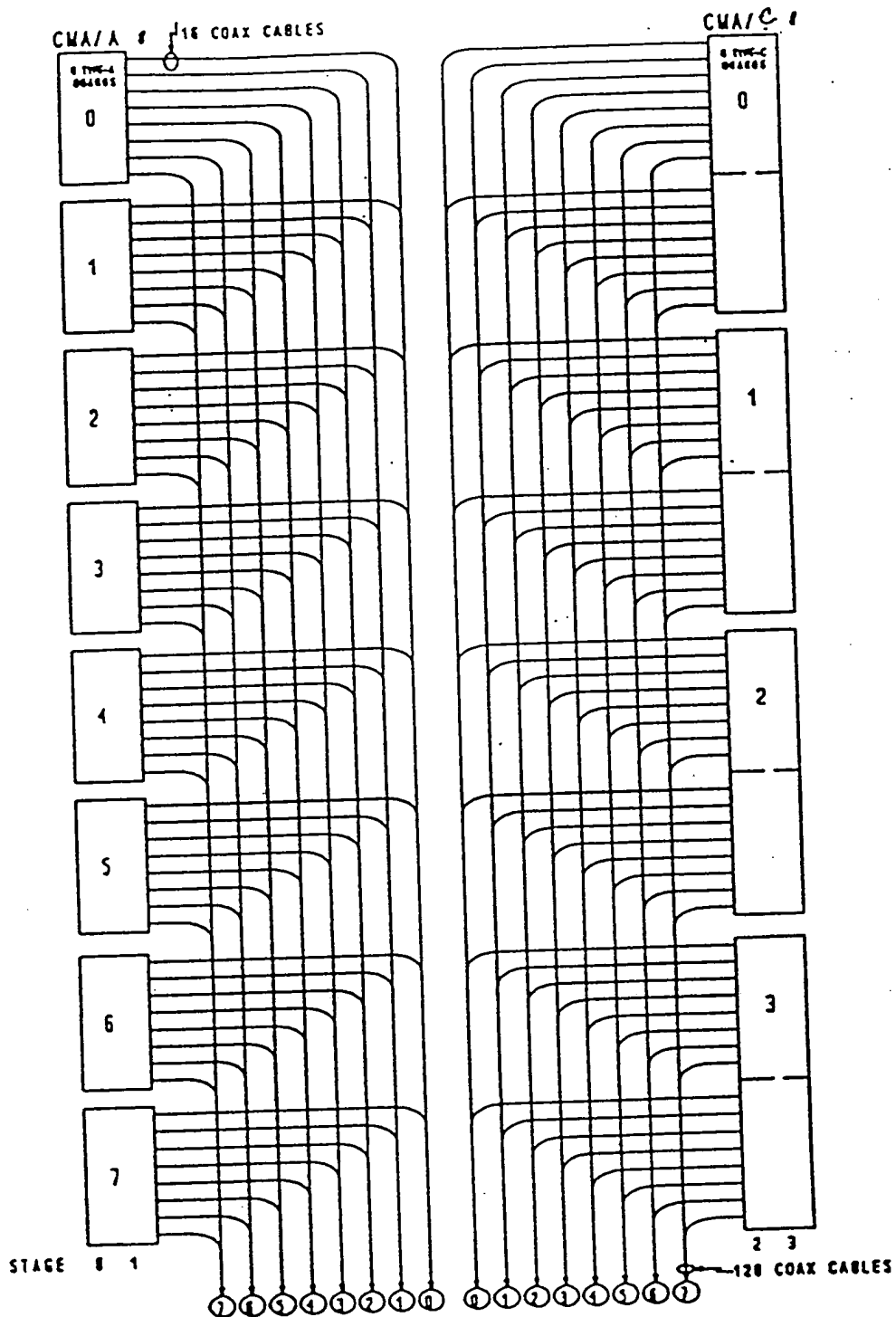


Figure 19

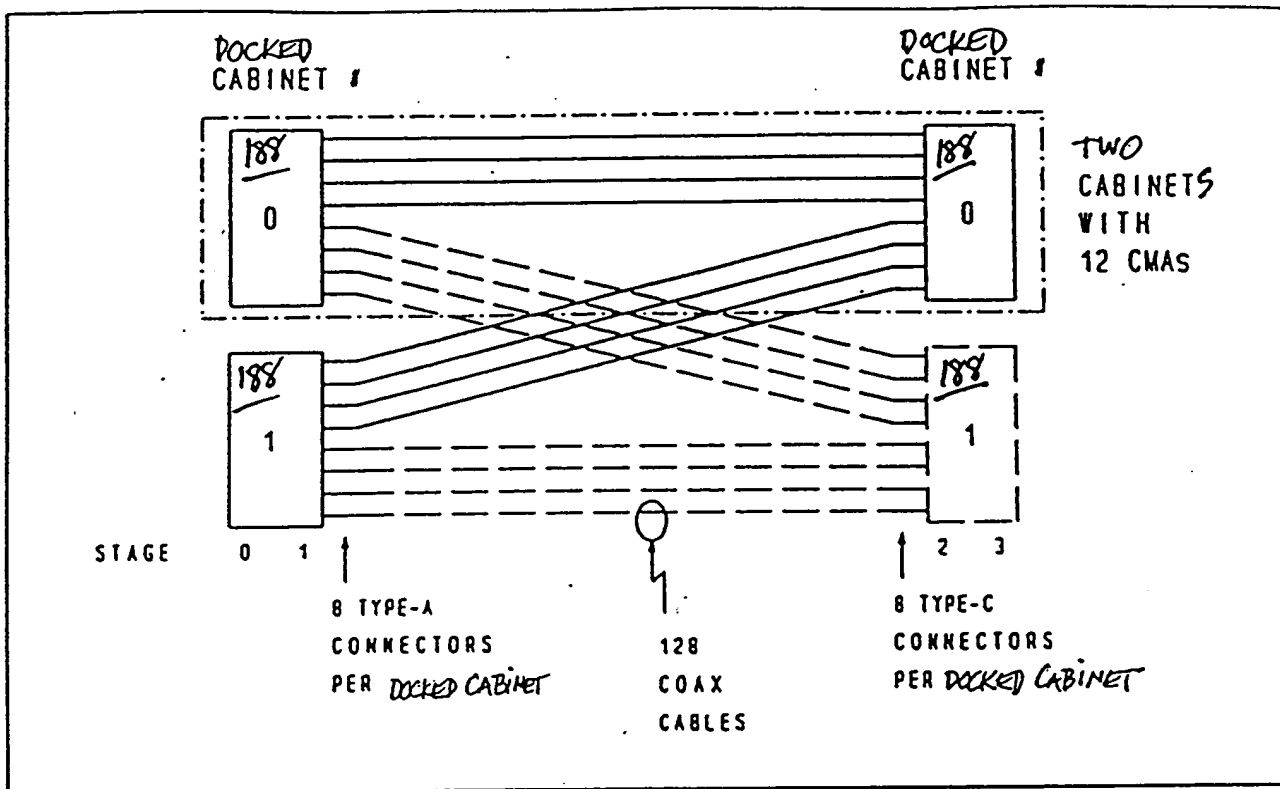


Figure 20

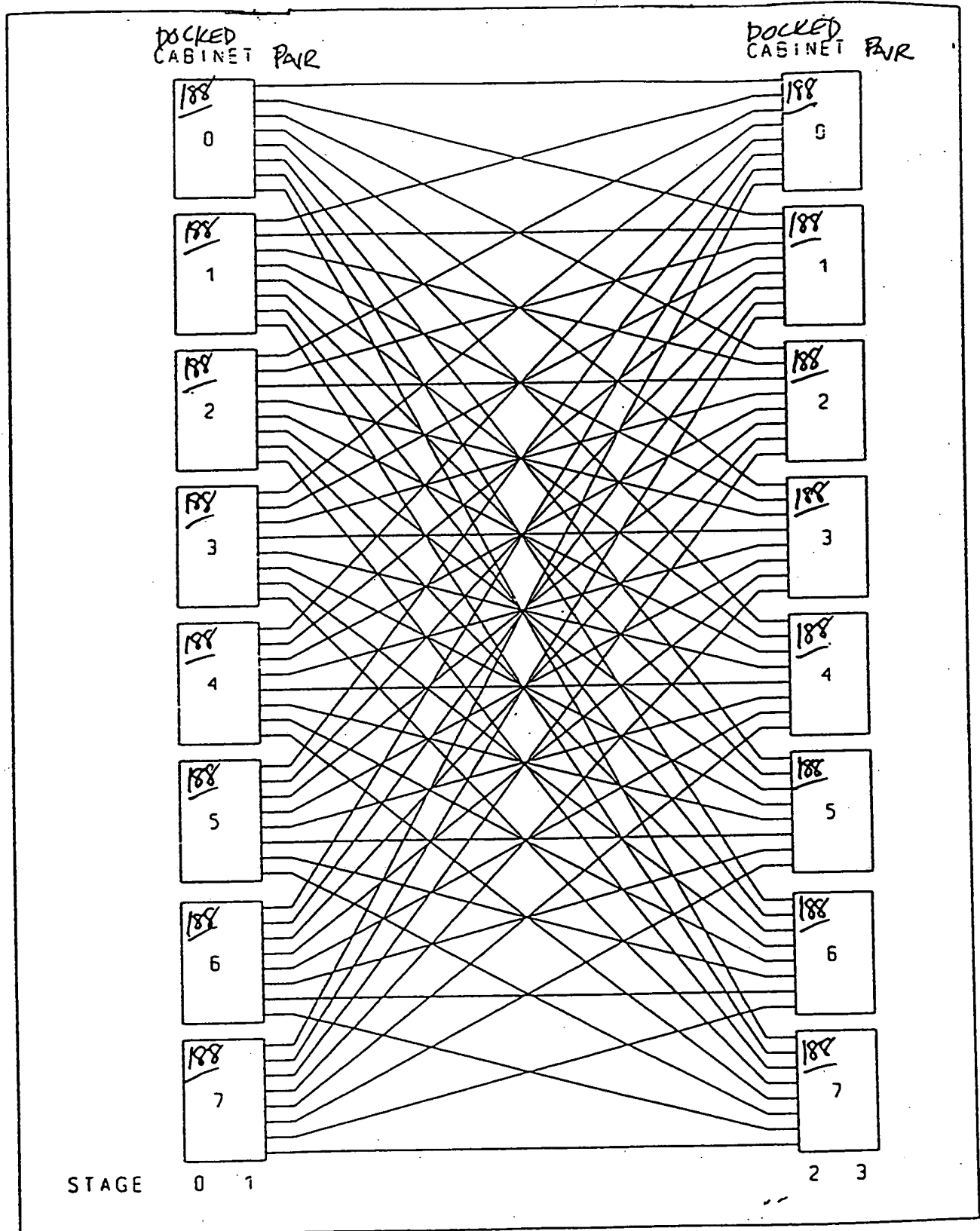


Figure 21

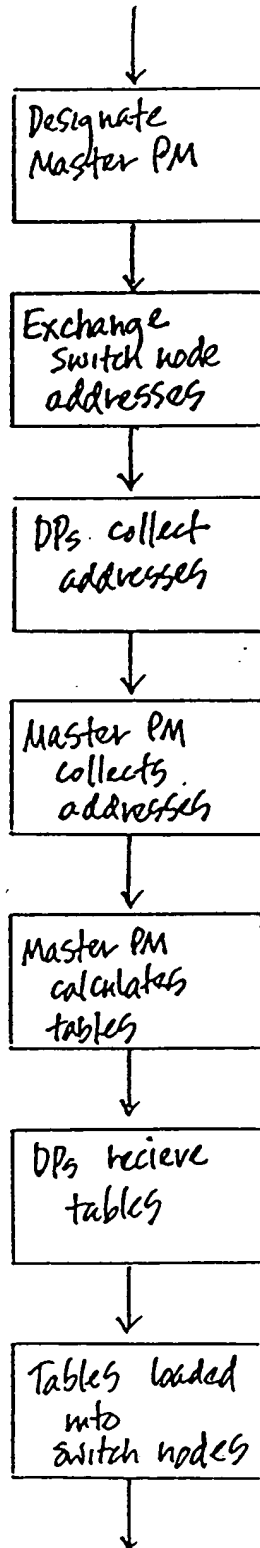


Figure 22

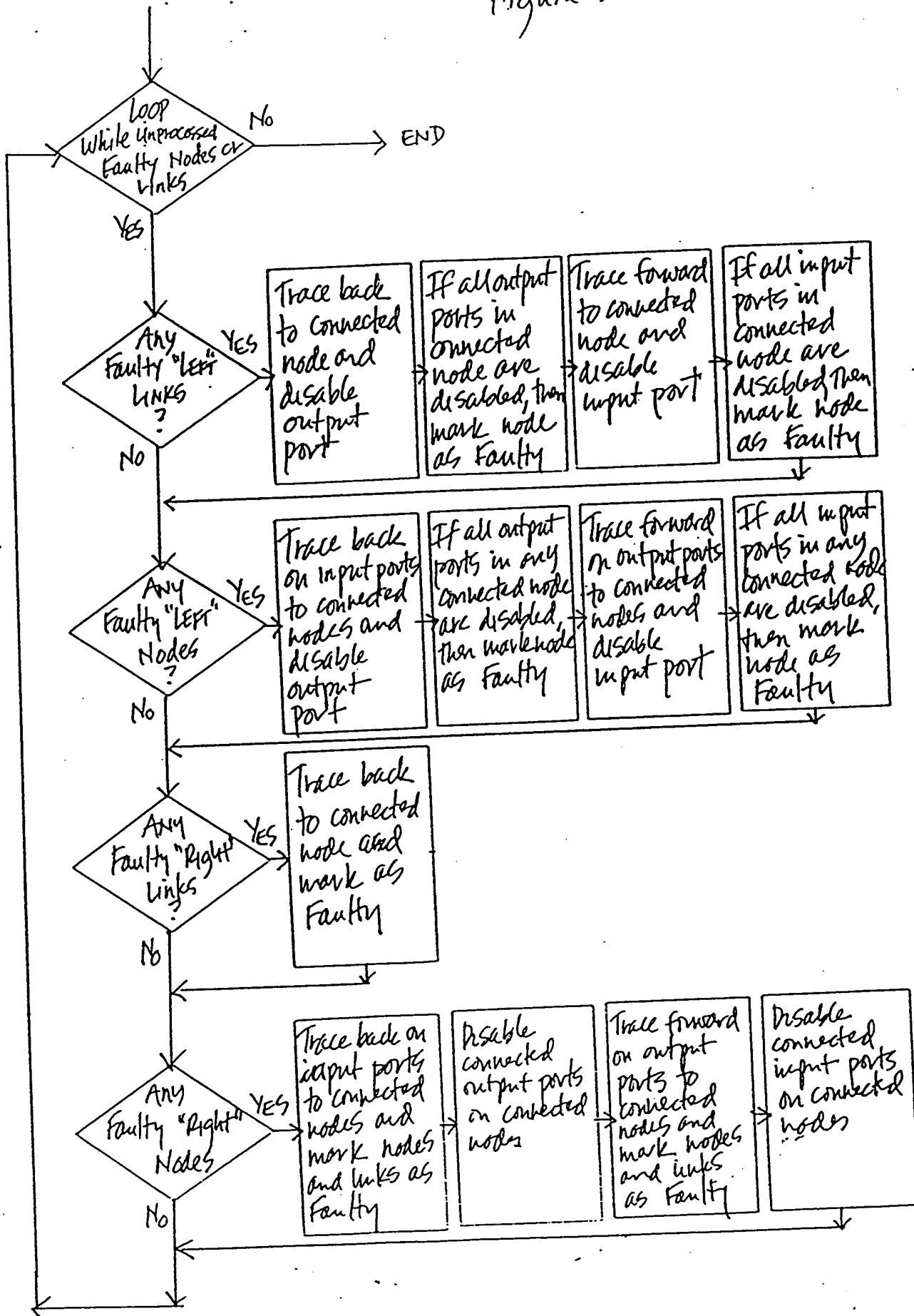


Figure 23

LOOP-BACK POINT 30

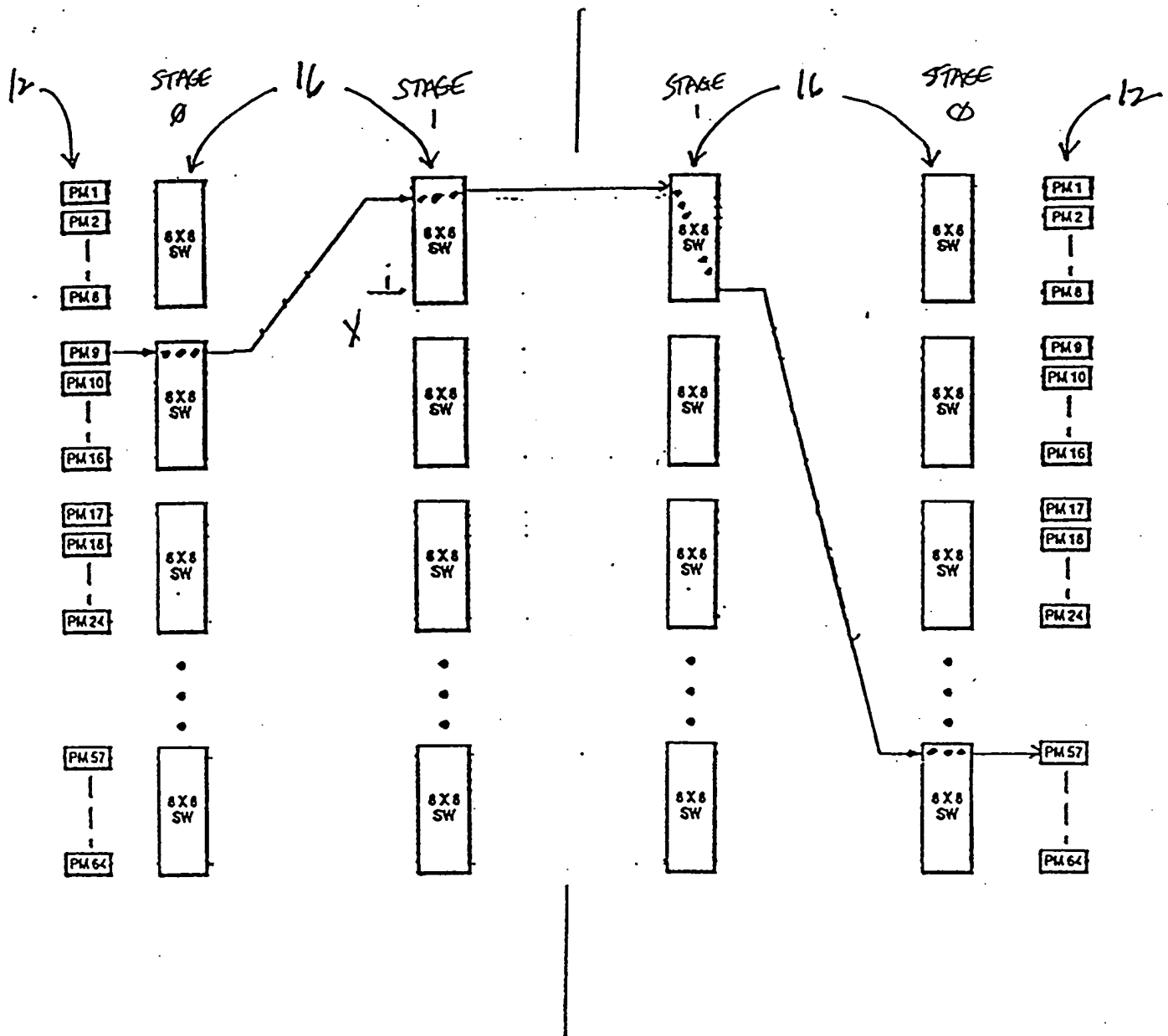


Figure 24

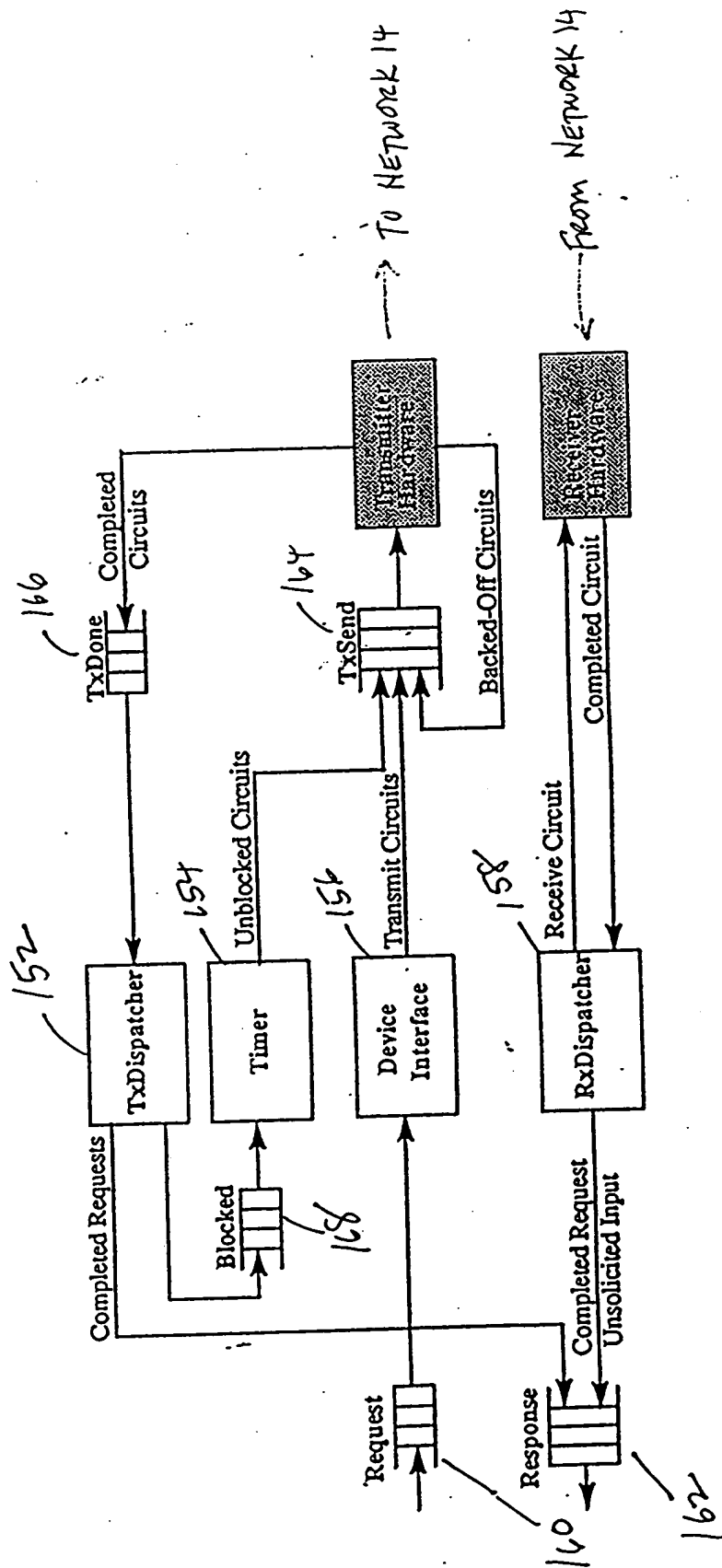


Figure 25.

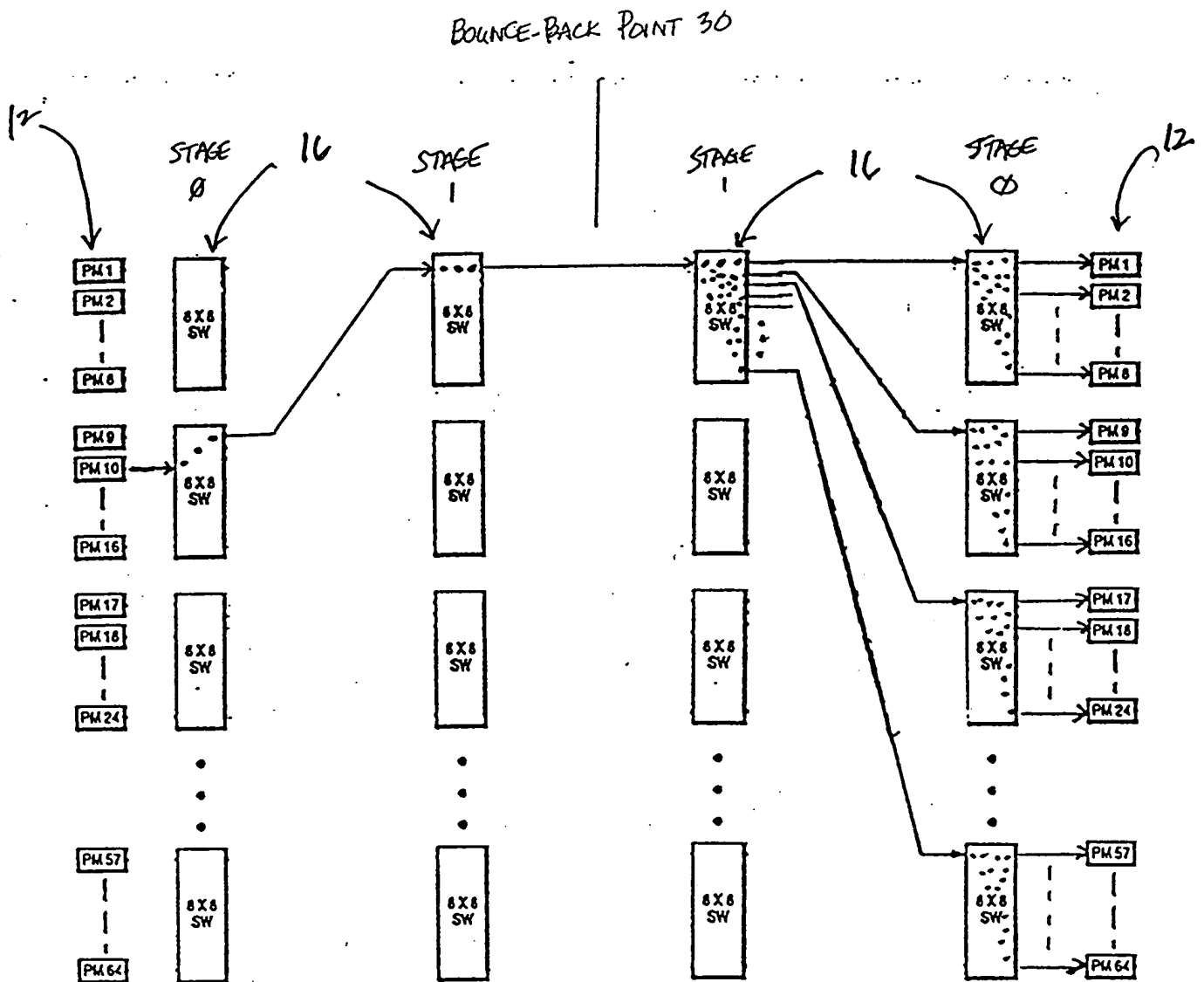


Figure 26

